

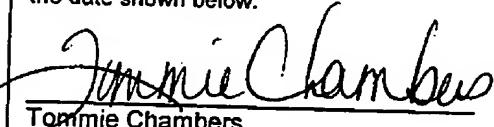
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To: Technology Unit: 2816  
Facsimile Number: 571-273-8300

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From: W. Daniel Swayze, Jr.  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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NAME OF INVENTOR(S): Pentakota		RECEIPT DATE & SERIAL NO.:	
TITLE OF INVENTION: LOW NOISE OUTPUT BUFFER		Serial No.: 10/775,022	
TI FILE NO.: TI-37302		Filing Date: 2/9/2004	
DEPOSIT ACCT. NO.: 20-0668			
FAXED: 11-1-05 DUE: 10/5/2005 ATTY/SECY: WDS/tlc			

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Pentakota

Docket No: TI-37302

Serial No: 10/775,022

Examiner: Wells, Kenneth

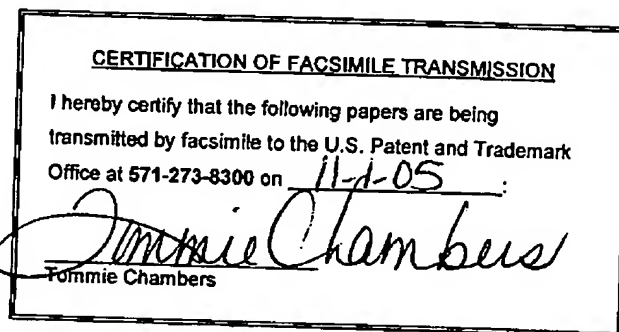
Filed: 2/9/2004

Art Unit: 2816

For: OUTPUT BUFFER FOR DIGITAL LOCK SIGNAL USING AN ANALOG  
AMPLIFIER, A CONVERTER CIRCUIT, AND A BIAS CIRCUIT

AMENDMENT UNDER 37 C.F.R. § 1.116

Assistant Commissioner For Patents  
Washington, DC 20231



Dear Sir:

Responsive to the Office Action mailed August 5, 2005 in connection with the  
above identified application, Applicants respectfully submit the following amendments  
and remarks.

**IN THE TITLE**

Please cancel the current title and substitute as the following from the new title.

**OUTPUT BUFFER FOR DIGITAL LOCK SIGNAL USING AN ANALOG AMPLIFIER,  
A CONVERTER CIRCUIT, AND A BIAS CIRCUIT**